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

temporary registers update flush processor

-  [AMD Geode GX Processors Silicon Revision 2.1 Specification Update](#) - 10:17pm
www.amd.com/.../geode_gx/31533E_gx_2.1_specupdate.pdf
-  [Computer Architecture](#) - 10:17pm
thieumswb.free.fr/english/classesca.html

architected registers update flush

-  [OS and Compiler Considerations in the Design of the IA-64 Architecture](#) - 10:16pm
h21007.www2.hp.com/.../unprotected/IA64oscompilercon.pdf
-  [IBM Journal of Research and Development: Organization and ...](#) - 10:16pm
www.findarticles.com/.../is_200501/ai_n9521087/pg_27
-  [Partitioning Register File to Reduce Access Time](#) - 10:15pm
www.owlnet.rice.edu/~elec525/projects/prf_report.pdf

pipeline flush update flags

-  [The Problem Adding a Branch Predictor to your Pipeline Model](#) - 10:15pm
www.cse.nd.edu/courses/cse30322/www/Labs/BPredProj.pdf
-  [Multiprogramming Performance of the Pentium 4 with Hyper-Threading](#) - 10:15pm
www.ece.wisc.edu/~wddd/2004/06_bulpin.pdf
-  [Data processing systems - Patent 4112489](#) - 10:14pm
www.freepatentsonline.com/4112489.html

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	371	eflags	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/03/28 09:33
S2	507	(712/218).CCLS.	US-PGPUB; USPAT; USOCR; IBM_TDB	OR	OFF	2006/03/28 09:51
S3	203	S2 AND conditional	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/03/28 10:05
S4	200	renam\$1 with branch\$4	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/03/28 10:05
S5	678	renam\$4 with branch\$4	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/03/28 10:05
S6	9	renam\$4 with branch\$4 WITH valid	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/03/28 10:05
S7	12866	status register	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/03/31 18:30
S8	2520	temporary register	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/03/31 18:30
S9	339	speculative register	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/03/31 18:32
S10	337	speculative register NOT WITH branch	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/03/31 18:32
S11	122	speculative register NOT SAME branch	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/03/31 18:32

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S12	122	speculative register NOT SAME branch\$3	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/03/31 18:32
S13	122	(speculative register) NOT SAME branch\$3	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/03/31 18:33
S14	782	store queue	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/03/31 18:34
S15	243	S14 AND "712".CLAS.	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/03/31 18:34
S16	638	flush\$3 WITH copy\$4	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/03/31 18:36
S17	52	S16 and checkpoint\$4	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/03/31 18:39
S18	308	checkpoint\$4 AND "712".CLAS.	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/03/31 18:39
S19	248	scalar WITH advantag\$2	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/04/01 13:55
S20	309	scalar WITH advantag\$5	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/04/01 13:56
S21	192	S20 AND "7"???.CLAS.	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/04/01 13:57
S22	0	S20 AND "7"???.CLAS. WITH (simpl\$5 OR complex\$5)	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/04/01 13:57
S23	19	S20 WITH (simpl\$5 OR complex\$5) AND "7"???.CLAS.	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/06/21 22:19

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S24	320	scalar WITH advantag\$5	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/06/21 22:19
S25	20	S24 WITH (simpl\$5 OR complex\$5) AND "7"?.CLAS.	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/06/21 22:20
S26	200	S24 AND "7"?.CLAS.	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/06/21 22:20
S27	379	eflags	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/06/21 22:20
S28	200	S24 AND "7"?.CLAS.	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/06/21 22:20
S29	20	S24 WITH (simpl\$5 OR complex\$5) AND "7"?.CLAS.	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/06/21 22:20
S30	272	(update or copy OR move OR refresh) NEAR5 ((architect\$6 OR physical\$4) NEAR2 register\$1)	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/06/21 22:24
S31	0	(update or copy OR move OR refresh) NEAR5 ((architect\$6 OR physical\$4) NEAR2 register\$1) WITH (flush Or clear)	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/06/21 22:24
S32	0	((update or copy OR move OR refresh) NEAR5 ((architect\$6 OR physical\$4) NEAR2 register\$1)) WITH (flush OR clear)	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/06/21 22:24
S33	11	((update or copy OR move OR refresh) NEAR5 ((architect\$6 OR physical\$4) NEAR2 register\$1)) SAME (flush OR clear)	US-PGPUB; USPAT; USOCR; IBM_TDB	ADJ	OFF	2006/06/21 22:24